

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10781336
	Filing Date		2004-02-17
	First Named Inventor	Kaushik SAHA et al.	
	Art Unit	2193	
	Examiner Name	DO, Chat C.	
	Attorney Docket Number	2415-015-03	

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	7	5991787		1999-11-23	ABEL ET AL.	
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9	6247034	B1	2001-06-12	NAKAI ET AL.	
10	6304887	B1	2001-10-16	JU ET AL.	
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1	1426872	EP	A2	2004-06-09	STMICROELECTRONIC S PVT. LTD.		<input type="checkbox"/>
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	1	DAWOU (2002) "AN EFFECTIVE MEMORY ADDRESSING SCHEME FOR MULTIPROCESSOR FFT SYSTEM"; 2002 6TH IEEE AFRICON CONFERENCE, UNIV. OF PRETORIA, GEORGE., SOUTH AFRICA; 1:29-34	<input type="checkbox"/>
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